Group Art Unit: 2833 Examiner: Luebke, R.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

olicant

Kazunori Yamashita

Appl. No.

10/611,762

Filed

July 1, 2003

For

CIRCUIT BOARD

CONNECTOR

METHOD AND

Atty. Ref.: FP03-013US

OF

ASSEMBLING IT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

ELECTION

Sir:

This Election is submitted in response to the Restriction Requirement of The applicant hereby elects claims 1-9 which are drawn to an January 2, 2004. electrical connector. This election is made without traverse.

Respectfully submitted,

Atty. Reg. No. 30,066

Customer No. 001218

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:

> Commissioner for Patents . . . P.O. Box 1450 Alexandria, VA 22313-1450

Date: January 15, 2004